

FORM PTO-1449 SAMUELS, GAUTHIER & JOHNSON LLP
(Rev. 5/92) 225 Franklin Street, Boston, MA 02110
Telephone: (617) 426-9220

ATTORNEY DOCKET NO. 5

SERIAL NO. 09/884,172

Fitzgerald et al.
APPLICANT

Unknown
GROUP

6/19/01
FILING DATE

Unknown
EXAMINER

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

AUG 01 2001

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<i>E</i>	AA	4,710,788	12/1/87	Dämbkes et al.	_____	_____	12/1/86
<i>E</i>	AB	5,241,197	8/31/93	Murakami et al.	_____	_____	9/13/91
<i>E</i>	AC	5,534,713	6/9/96	Ismail et al.	_____	_____	5/20/94
<i>E</i>	AD	5,777,347	7/7/98	Bartelink	_____	_____	6/18/97
<i>E</i>	AE	5,808,344	9/15/98	Ismail et al.	_____	_____	2/4/97
<i>E</i>	AF	5,906,951	5/25/99	Chu et al.	_____	_____	4/30/97
<i>E</i>	AG	6,013,134	1/11/00	Chu et al.	_____	_____	2/18/98
<i>E</i>	AH	6,059,895	5/9/00	Chu et al.	_____	_____	5/13/99
<i>E</i>	AI	6,111,367	8/29/00	Fischer et al.	_____	_____	5/4/98
<i>E</i>	AJ	6,107,653	8/22/00	Fitzgerald	_____	_____	6/23/98

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	AK						
	AL						
	AM						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER INITIAL		
<i>E</i>	AN	J. Welser, "The Application of Strained Silicon/Relaxed Silicon Germanium Heterostructures to Metal-Oxide-Semiconductor Field-Effect Transistors," PhD Thesis, Stanford University, 1995, pgs: 1-205
<i>E</i>	AO	"Technology for SiGe Heterostructure-Based CMOS Devices" by Mark Albert Armstrong; PhD Thesis, Massachusetts Institute of Technology, 1999; pgs: 1-154
<i>E</i>	AP	"Application of Silicon-Based Heterostructures To Enhanced Mobility Metal-Oxide-Semiconductor Field-Effect Transistors" by Kern Rim; PhD Thesis, Stanford University, 1999; pgs: 1-184
<i>E</i>	AQ	"Fabrication and Analysis of Deep Submicron Strained-Si N-MOSFET's"; Rim et al.; IEEE Transactions on Electron Devices, Vol. 47, No. 7, July 2000; pgs: 1406-1415
<i>E</i>	AR	"Enhanced Hole Mobilities in Surface-channel Strained-Si p-MOSFETs"; Rim et al.; Solid State Electronics Laboratory, Stanford University, Stanford, CA 94305; pgs: 20.3.1-20.3.4
<i>E</i>	AS	"High-Mobility Strained-Si PMOSFET's"; Nayak et al.; IEEE Transactions On Electron Devices, Vol. 43, No. 10, October 1996; pgs: 1709-1716
<i>E</i>	AT	"Electron and Hole Mobility Enhancement in Strained-Si MOSFET's on SiGe-on-Insulator Substrates Fabricated by SIMOX Technology"; by Mizuno et al.; IEEE Electron Device Letters, Vol. 21, No. 5, May 2000; pgs: 230-232

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DATE CONSIDERED

Christopher Lattin

9/20/02

EXAMINER:

Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 SAMUELS, GAUTIER & STEVENS, LLP
(Rev. 5/92) 225 Franklin Street, Boston, MA 02110
Telephone: (617) 426-9180

ATTORNEY DOCKET NO. 5994B

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**INFORMATION DISCLOSURE
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Fitzgerald et al.
APPLICANT

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EXAMINER

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	6,111,267	8/29/00	Fischer et al.			5/4/98
	AB						
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FOREIGN PATENT DOCUMENTS

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FORM PTO-1449 SAMUELS, GAUTHIER & STEVENS LLP
(Rev. 5/92) 225 Franklin Street, Boston, MA 02116
Telephone: (617) 426-9180

ATTORNEY DOCKET NO. 5994B

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Fitzgerald et al.
APPLICANT

Unknown
GROUP 2812

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EXAMINER Lathin

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Q	AA	6,111,267	8/29/00	Fischer et al.			5/4/98
	AB						
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	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

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	AL						
	AM						

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EXAMINER INITIAL		
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Christopher Lathin

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